

AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph no. 19 with the following amended paragraph:

According to an aspect of the present invention, there is provided a flash memory comprising a map block. The map block includes a first mapping table containing a physical address allocated to each of blocks constituting a data block and status information of each of the blocks, a second mapping table containing mapping information between the physical address and a ~~local~~logical address on each of the blocks in the first mapping table from which error blocks are excluded, and a third mapping table in which most recent mapping information is written and processed by a specified value to minimize an update operation of the second mapping table.